

WHAT IS CLAIMED IS:

1. A signal processing circuit for use in a broadcast receiving apparatus for receiving a broadcast signal, the broadcast
5 receiving apparatus including a tuning request storage section, the signal processing circuit comprising:

a tuning request reception section for receiving an issued tuning request;

10 a tuning process section for performing a tuning process for the broadcast signal corresponding to the issued tuning request, wherein, if at least one tuning request is issued during the tuning process, the tuning process section performs a next tuning process corresponding to one of the at least one tuning request stored in the tuning request storage section during the tuning process; and

15 a tuning request erasure section for erasing, at a predetermined point in time, the at least one tuning request stored in the tuning request storage section during the tuning process,

wherein the tuning request received by the tuning request reception section is stored to the tuning request storage
20 section.

2. The signal processing circuit according to claim 1, wherein the tuning request erasure section erases the at least one tuning request stored in the tuning request storage section at the
25 beginning of the tuning process.

3. The signal processing circuit according to claim 1, wherein
if the tuning process fails, the tuning process section
performs a further tuning process corresponding to one of the tuning
5 requests stored in the tuning request storage section during a
previous tuning process, and

the tuning request erasure section erases the at least one
tuning request stored in the tuning request storage section during
the previous tuning process after the further tuning is completed.

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4. The signal processing circuit according to claim
1, wherein, if at least one tuning request is issued during the tuning
process, the tuning process section performs a next tuning process
corresponding to a most recent tuning request among the at least one
15 tuning request stored in the tuning request storage section.

5. The signal processing circuit according to claim 1, wherein
the tuning requests are issued by a software application.